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APPLICATION NO.	F	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/938,106 08/2		08/23/2001	James M. Derderian	4832US (01-0104)	1038	
24247	7590	03/27/2006		EXAMINER		
TRASK BI	RITT		IM, JUNGHWA M			
P.O. BOX 2 SALT LAK		UT 84110		ART UNIT	PAPER NUMBER	
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				DATE MAILED: 03/27/2006		

Please find below and/or attached an Office communication concerning this application or proceeding.



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APPLICATION NO./ CONTROL NO.	FILING DATE	FIRST NAMED INVENTOR / PATENT IN REEXAMINATION		ATTORNEY DOCKET NO. EXAMINER	
			ART UNIT	PAPER	
				20060316	

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Commissioner for Patents

The reply brief filed February 16, 2006 has been entered and considered. The application has been forwarded to the Board of Patent Appeals and Interferences for decision on the appeal.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Junghwa M. Im whose telephone number is (571) 272-1655. The examiner can normally be reached on MON.-FRI. 8:30 AM-5:00 PM. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie C Lee can be reached on (571) 272-1732. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

jmi

EDDIE LEE

SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800





IN THE UNITED STATES PATENT AND TRADEMARK OFFICE BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

In re Application of: James M. Derderian

Serial No.: 09/938,106

Filed: August 23, 2001

For: ASSEMBLIES INCLUDING STACKED SEMICONDUCTOR DEVICES SEPARATED BY DISCRETE CONDUCTIVE ELEMENTS THEREBETWEEN, PACKAGES INCLUDING THE ASSEMBLIES, AND

METHODS

Confirmation No.: 1038

Examiner: J. Im

Group Art Unit: 2811

Attorney Docket No.: 2269-4832US

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REPLY BRIEF

Mail Stop Appeal Brief - Patents Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Attn: Board of Patent Appeals and Interferences

Sirs:

This Reply Brief follows the Examiner's Answer of December 15, 2006, and is being filed pursuant to 37 C.F.R. § 41.41.